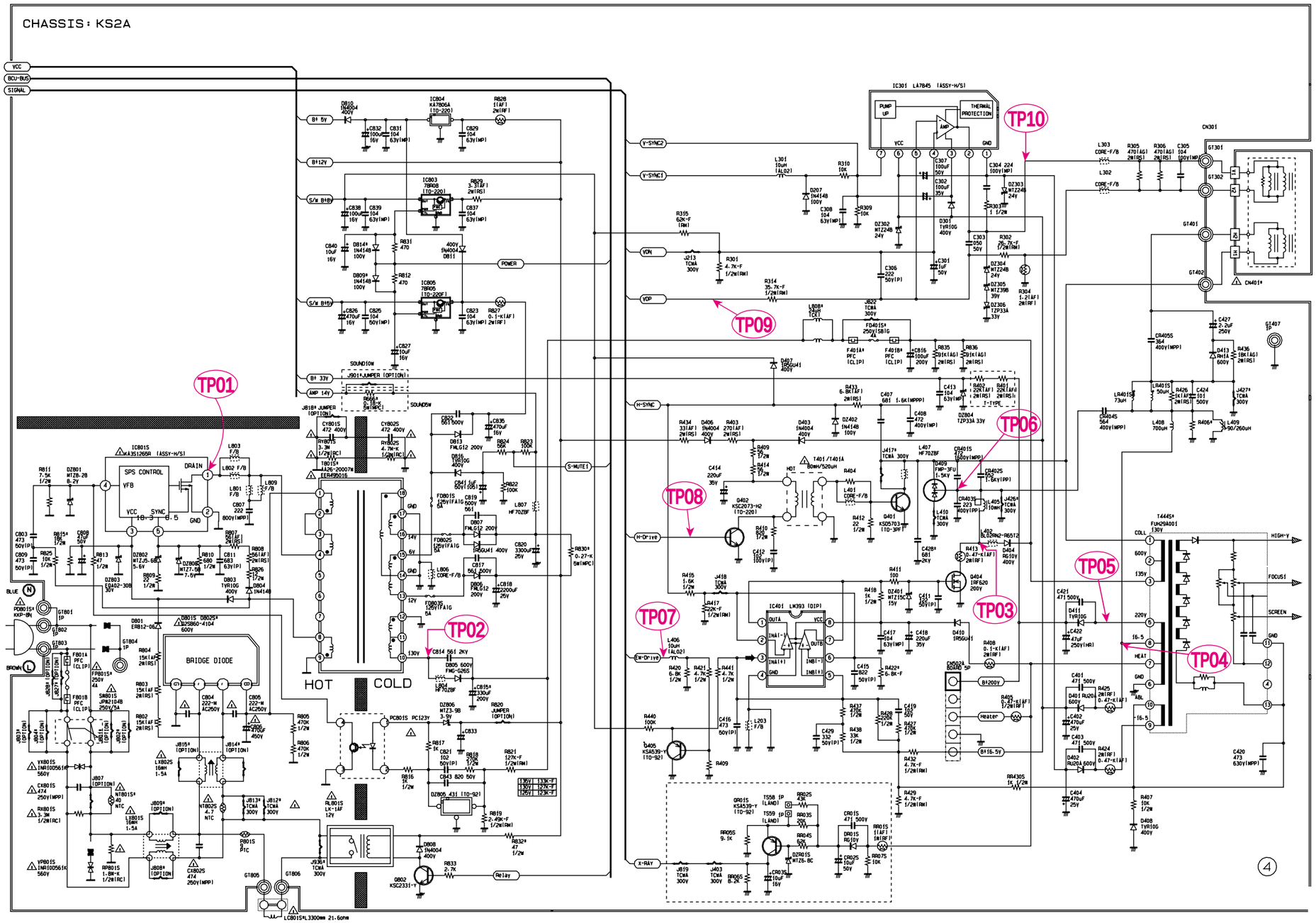
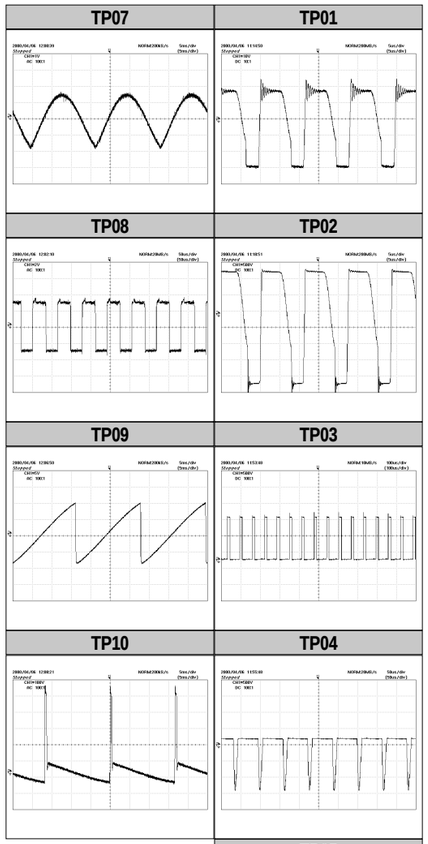
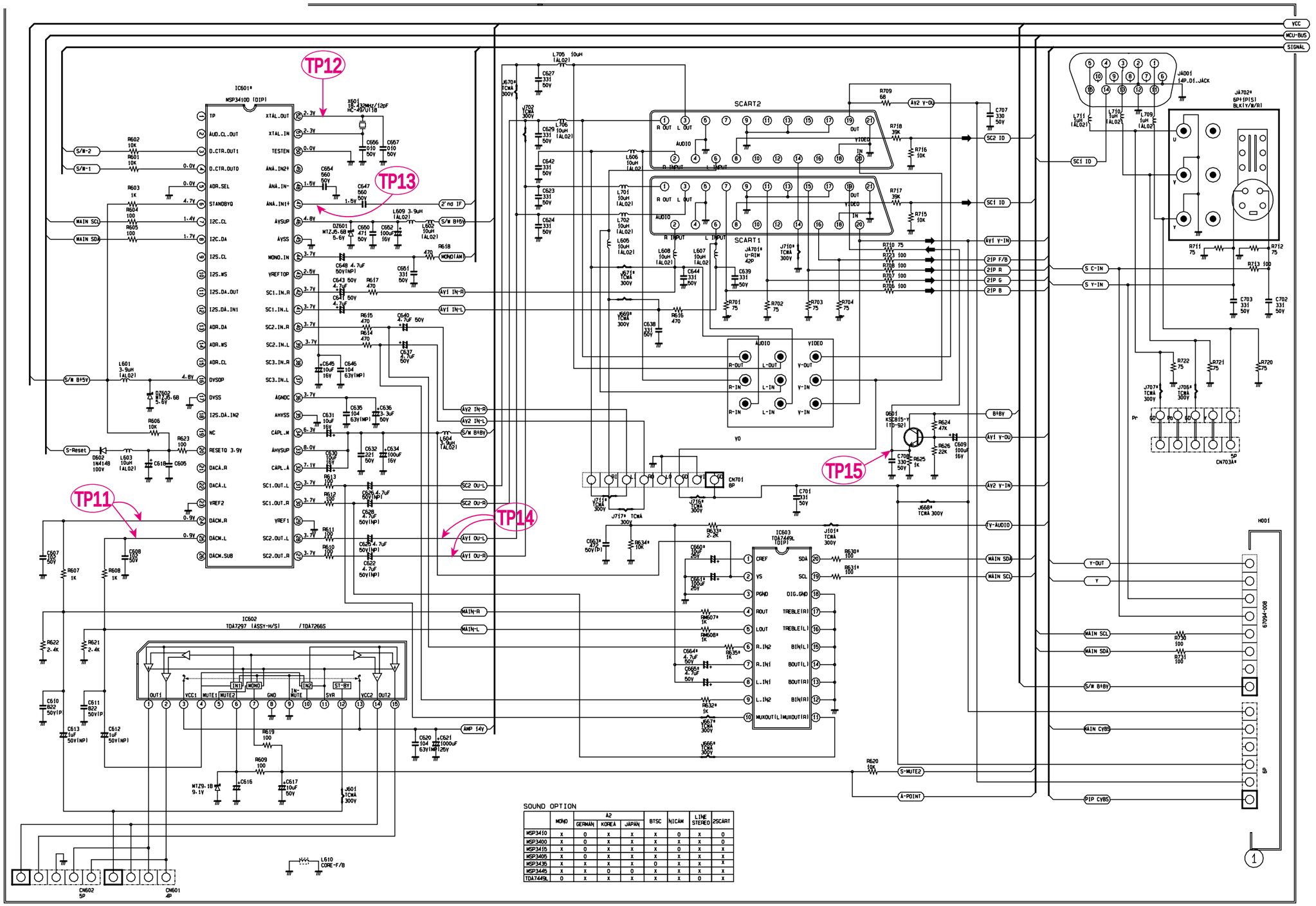
10. Schematic Diagrams

10-1 MAIN 1/4 (WITH EW)



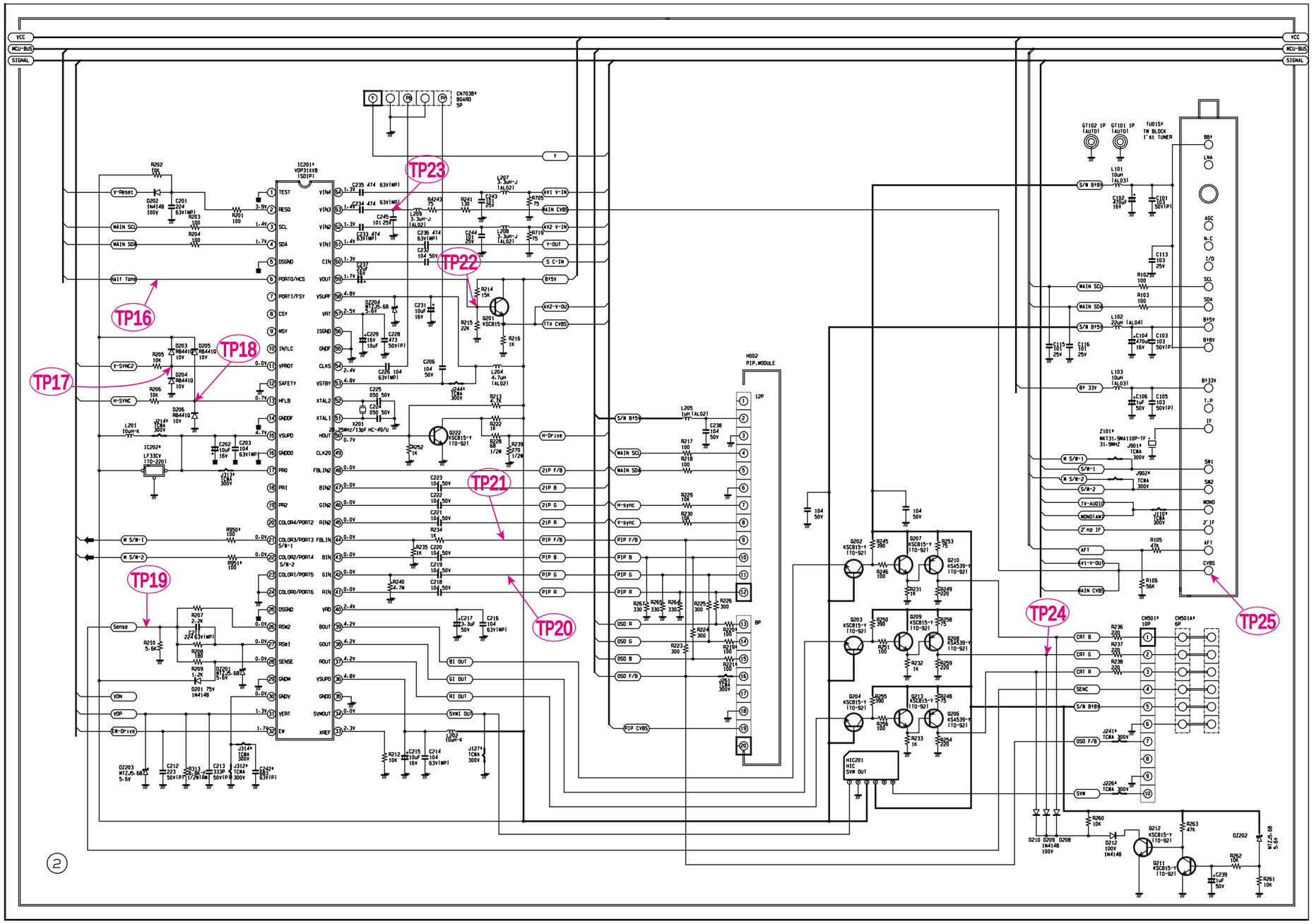


10-2 MAIN 2/4

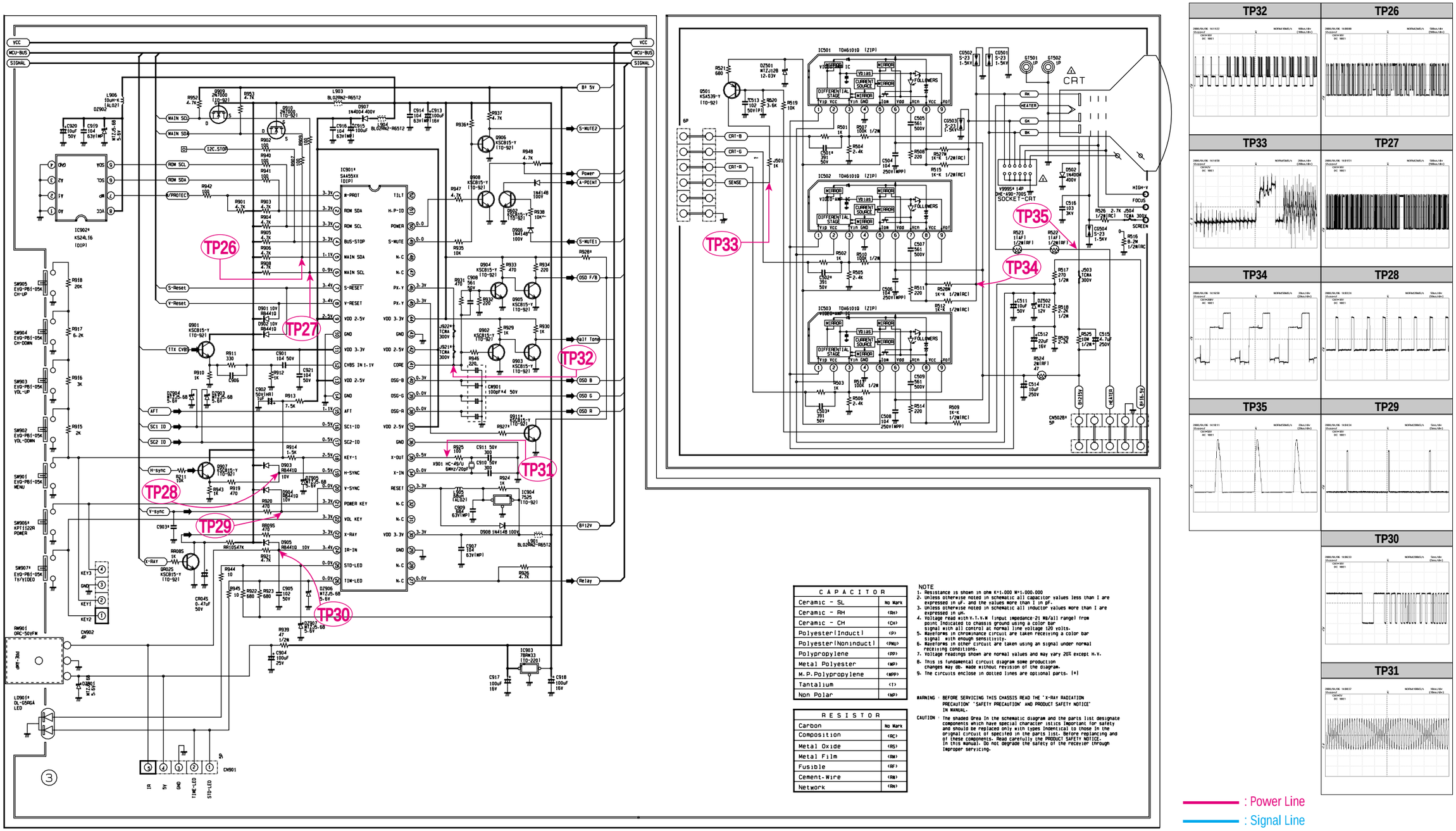


	MONO	A2 GERMAN	A2 KOREA	A2 JAPAN	BTSC	NICAM	LINE STEREO	2SCART
MSP3410	x	0	x	x	x	0	x	0
MSP3400	x	0	x	x	x	x	x	0
MSP3415	x	0	x	x	x	0	x	x
MSP3405	x	0	x	x	x	x	x	x
MSP3435	x	x	x	x	0	x	x	x
MSP3445	x	x	0	0	x	x	x	x
TDA7449L	0	x	x	x	x	x	0	x

10-3 MAIN 3/4


TP16
TP17
TP18
TP19
TP20
TP21
TP22
TP23
TP24
TP25
VCC
MCU-BUS
SIGNAL
IC201*
VDP31XXB
(SDIP)
TEST
RESO
SCL
SDA
DSGND
PORT0/HCS
PORT1/FSY
CSY
MSY
INTLC
VPROT
SAFETY
HFLB
GNDOF
VSUPO
GNDDO
PRO
PR1
PR2
COLOR4/PORT2
COLOR3/PORT3
COLOR2/PORT4
COLOR1/PORT5
COLOR0/PORT6
DSGND
RSW2
RSW1
SENSE
GNDM
GNDV
VERT
EW
VIN4
VIN3
VIN2
VIN1
CIN
VOUT
YSUB
VRT
ISGND
GNDF
CLKS
VSTBY
XTAL2
XTAL1
HOUT
CLK20
FBLIN2
BIN2
G1N2
R1N2
FBLIN
BIN
G1N
R1N
YFD
BOUT
GOUT
ROUT
YSUPO
GNDO
SYMOUT
XREF
Y-Reset
MAIN SCL
MAIN SDA
Olf Tone
Y-SYNC2
H-SYNC
M S/W-1
M S/W-2
YDN
YDP
H-Drive
Sense
Y
AV1 V-IN
MAIN CVBS
AV2 V-IN
Y-OUT
S C-IN
B+5V
AV2-V-OU
TTx CVBS
H-Drive
21P F/B
21P B
21P G
21P R
PIP F/B
PIP B
PIP G
PIP R
OSD R
OSD G
OSD B
OSD F/B
PIP CVBS
BI OUT
GI OUT
RI OUT
SVHI OUT
S/W B+5V
MAIN SCL
MAIN SDA
H-Sync
V-Sync
PIP F/B
PIP B
PIP G
PIP R
OSD R
OSD G
OSD B
OSD F/B
PIP CVBS
HIC201
HIC
SVM OUT
H002
PIP_MODULE
12P
8P
CN703B*
BOARD
5P
TU01S*
TW BLOCK
1'st TUNER
GT102 1P
GT101 1P
BB+
LNA
AGC
N-C
I/O
SCL
SDA
B+5V
B+8V
B+33V
T-P
IF
SB1
SB2
MONO
2' IF
AFT
CVBS
S/W B+5V
MAIN SCL
MAIN SDA
S/W B+8V
B+ 33V
S/W-1
S/W-2
TV-AUDIO
MONO(AM)
2'nd IF
AFT
V1-V-OUT
MAIN CVBS
Z101*
NKT31.5MA
31.5MHZ
J901*
TCMA
300V
J902*
TCMA
300V
J910*
TCMA
300V
CN501*
10P
CN501A*
6P
CRT B
CRT G
CRT R
SENC
S/W B+8V
OSD F/B
SVM
J241*
TCMA 300V
J226*
TCMA 300V
: Power Line
: Signal Line

10-4 MAIN 4/4



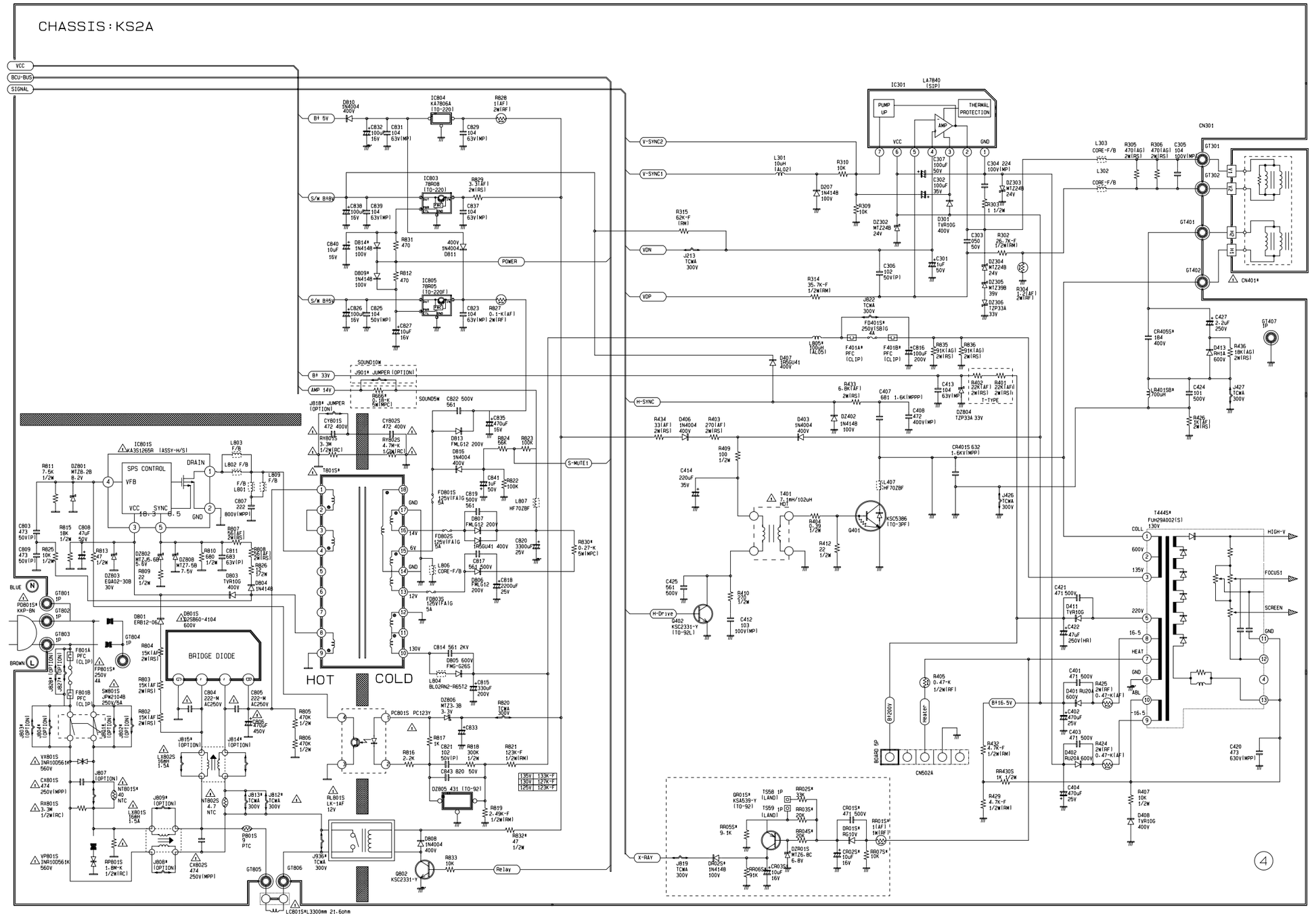
CAPACITOR	
Ceramic - SL	No Mark
Ceramic - RH	(RH)
Ceramic - CH	(CH)
Polyester(Inductl	(P)
Polyester(Noninductl	(PNL)
Polypropylene	(PP)
Metal Polyester	(MP)
M.P.Polypropylene	(MPP)
Tantalium	(T)
Non Polar	(NP)

RESISTOR	
Carbon	No Mark
Composition	(RC)
Metal Oxide	(RS)
Metal Film	(RN)
Fusible	(RF)
Cement-Wire	(RW)
Network	(RN)

NOTE
1. Resistance is shown in ohm K=1.000 M=1.000.000
2. Unless otherwise noted in schematic all capacitor values less than 1 are expressed in uF. and the values more than 1 in pF.
3. Unless otherwise noted in schematic all inductor values more than 1 are expressed in uH.
4. Voltage read with V.T.V.M (input impedance 2| MΩ/all) rangel from point indicated to chassis ground using a color bar signal with all control at normal line voltage 120 volts.
5. Waveforms in chrominance circuit are taken receiving a color bar signal with enough sensitivity.
6. Waveforms in other circuit are taken using an signal under normal receiving conditions.
7. Voltage readings shown are normal values and may vary 20% except H.V.
8. This is fundamental circuit diagram some production changes may OB. made without revision of the diagram.
9. The circuits enclose in dotted lines are optional parts. (*)

WARNING : BEFORE SERVICING THIS CHASSIS READ THE 'X-RAY RADIATION PRECAUTION' 'SAFETY PRECAUTION' AND PRODUCT SAFETY NOTICE' IN MANUAL.

CAUTION : The shades Grea in the schematic diagram and the parts list designate components which have special character istics important for safety and should be replaced only with types ihdentical to those in the original circuit of specified in the parts list. Before replancing and of these components. Read carefully the PRODUCT SAFETY NOTICE. In this manual. Do not degrade the safety of the receiver through improper servicing.

: Power Line
: Signal Line

10-5 POWER (W/O -EW)